

WHAT IS CLAIMED IS:

1. An integrated circuit comprising:
 - a bus;
 - a first memory connected to said bus;
 - a first processing unit operable to access said first memory via said bus;
 - a second processing unit operable to access said first memory via said bus, and operable to perform at least one of data processing and calculation in a larger amount than said first processing unit; and
 - a second memory operable to be accessed by said second processing unit without passing through said bus.
2. An integrated circuit as recited in claim 1, wherein said second processing unit comprises at least one of an image input circuit and an image display circuit.
3. An integrated circuit as recited in claim 1, wherein said first processing unit expands compressed audio signals,
 - wherein said second processing unit expands compressed video signals, and
 - wherein said second processing unit stores reference image data into said second memory, the reference image data being generated when the compressed video signals are expanded.
4. An integrated circuit as recited in claim 1, wherein said first processing unit compresses audio signals,
 - wherein said second processing unit compresses video signals, and
 - wherein said second processing unit stores reference image data into said second memory, the reference image data being generated when the compressed video signals are expanded.
5. An integrated circuit as recited in claim 1, wherein said first processing unit performs at least one of de-multiplexing audio signals and video signals from a bit stream and multiplexing audio signals and video signals into a bit stream.

6. An integrated circuit as recited in claim 1, wherein said second processing unit generates computer graphics image data.

7. An integrated circuit as recited in claim 1, further comprising a control unit operable to control at least one of said first processing unit and said second processing unit.

8. An electric device comprising:

an integrated circuit; and

a converter,

wherein said integrated circuit comprises:

a bus;

a first memory connected to said bus;

a first processing unit operable to access said first memory via said bus;

a second processing unit operable to access said first memory via said bus, and operable to perform at least one of data processing and calculation in a larger amount than said first processing unit; and

a second memory operable to be accessed by said second processing unit without passing through said bus,

wherein said first processing unit expands compressed audio signals,

wherein said second processing unit expands compressed video signals to generate video signals,

wherein said second processing unit stores reference image data into said second memory, the reference image data being generated when the compressed video signals are expanded, and

wherein said converter is operable to convert the audio signals expanded by said first processing unit into analogue audio signals.

9. An electric device as recited in claim 8, wherein said integrated circuit further comprises a control unit operable to control at least one of said first processing

unit and said second processing unit.

10. An electric device as recited in claim 8, further comprising:

a display device operable to input the video signals generated by said second processing unit to display an image; and

a playback device operable to reproduce sounds according to the analogue audio signals converted by said converter.

11. An electric device as recited in claim 8, wherein said second processing unit generates computer graphics image data.

12. An electric device comprising: a camera; a microphone; an integrated circuit; and a converter,

wherein said integrated circuit comprises:

a bus;

a first memory connected to said bus;

a first processing unit operable to access said first memory via said bus;

a second processing unit operable to access said first memory via said bus, and operable to perform at least one of data processing and calculation in a larger amount than said first processing unit; and

a second memory operable to be accessed by said second processing unit without passing through said bus,

wherein said first processing unit compresses audio signals,

wherein said second processing unit inputs video signals from said camera to compress the video signals,

wherein said second processing unit stores reference image data into said second memory, the reference image data being generated when the compressed video signals are expanded, and

wherein said converter is operable to input analogue audio signals from said microphone to convert the analogue audio signals into digital audio signals, and

operable to output the digital audio signals to said first processing unit.

13. An electric device as recited in claim 12, wherein said second processing unit generates computer graphics image data.

14. An electric device as recited in claim 12, wherein said integrated circuit further comprises a control unit operable to control at least one of said first processing unit and said second processing unit.